

## IN THE CLAIMS

Claims 1-31 were originally presented in the application. Claims 1-6 and 19-31 were cancelled in a preliminary amendment. Claims 7-14 are cancelled in the instant response. Claim 15 is an independent claim and claims 16-18 are dependent claims, depending from claim 15. Applicants respectfully request reconsideration of pending claims as recited below.

Please amend the claims as follows.

1-14. (Cancelled)

15. (Currently Amended) A digital signal processor for processing a multiple frame video digital signal, comprising:

a DSP controller,

a plurality of processing units connected to said DSP controller for processing said multiple frame video digital signal; and

at least one storage unit, wherein each of said processing units is connected to at ~~lest~~ least one of said at least one storage units,

said DSP controller controlling said plurality of processing units,

wherein said DSP controller, said plurality of processing units, and said at least one storage unit are on a single chip.

16. (Original) The digital signal processor according to claim 15 wherein each of said processing units is operative to access any storage address of any of said at least one storage unit, connected thereto.

17. (Original) The digital signal processor according to claim 15 wherein each of said processing units operates according to a different program command.

18. (Original) The digital signal processor according to claim 15 wherein each of said processing units operates on a different portion of data.

19-31. (Cancelled)